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Fig.1 (Prior Art)

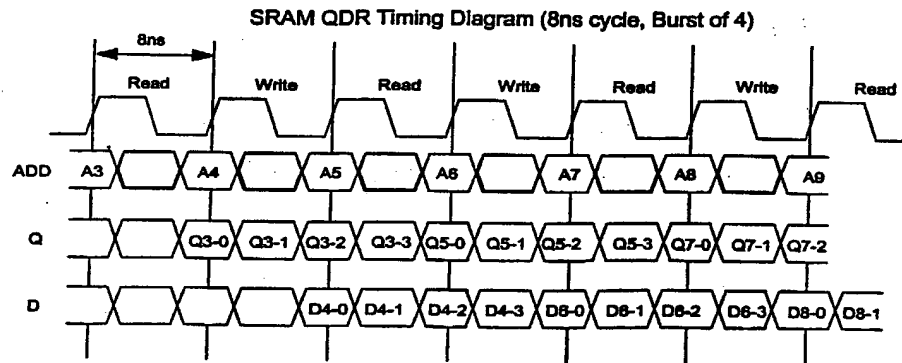
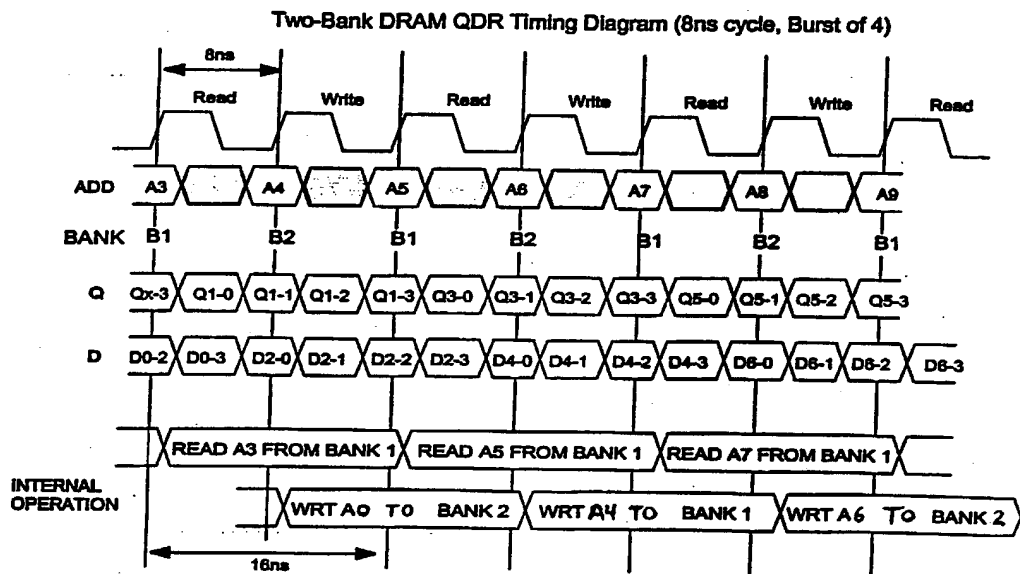


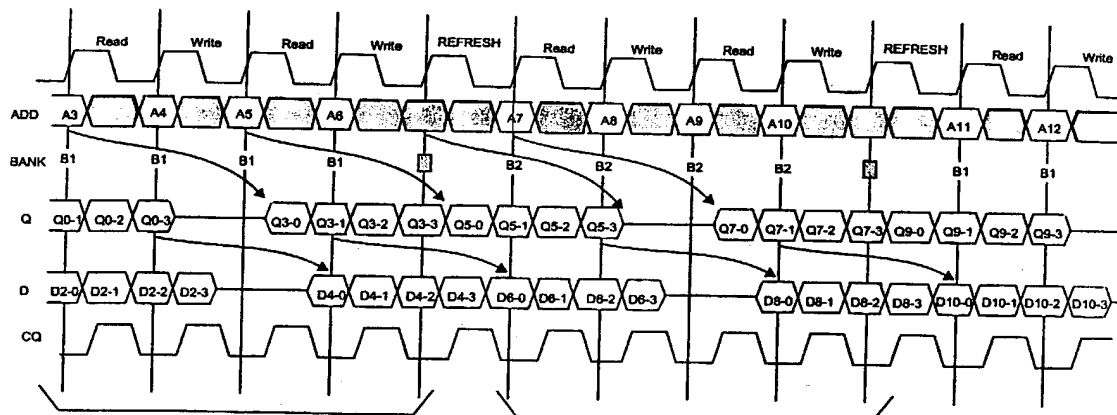
Fig.2 (Prior Art)



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Fig. 3

QDR 4-Burst 2-Bank Timing Diagram: Read-Write

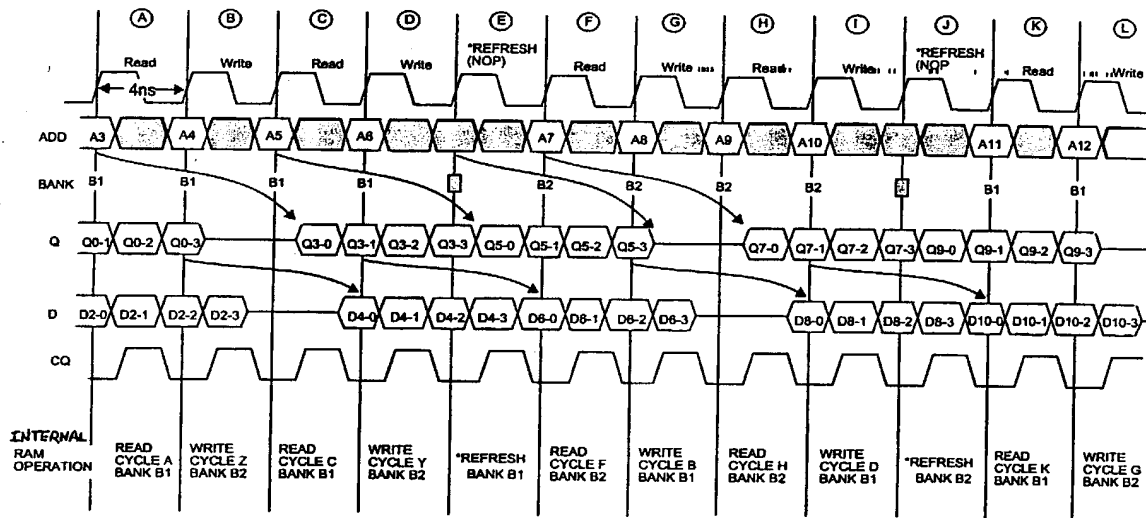


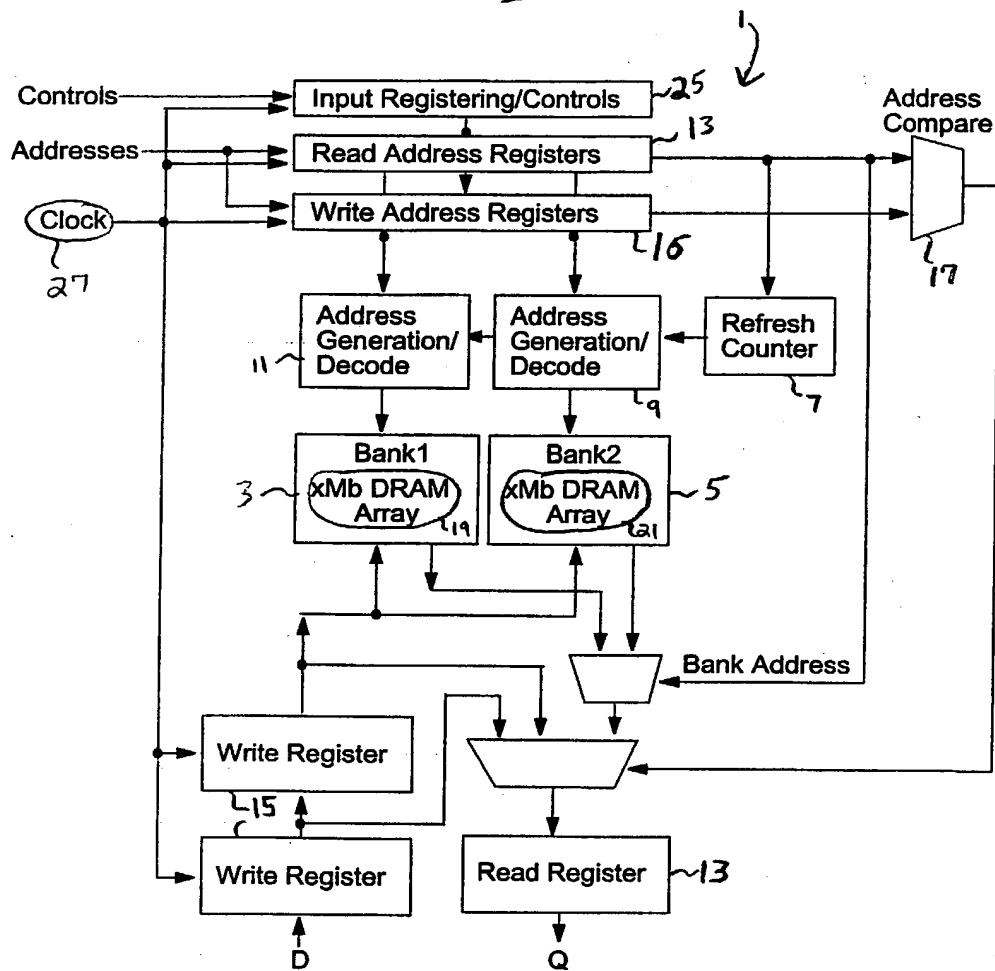
FOUR READ/WRITE OPERATIONS WITHIN THE SAME BANK ARE SHOWN, BUT CAN BE TWO, FOUR, SIX, EIGHT, ...

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Fig. 4

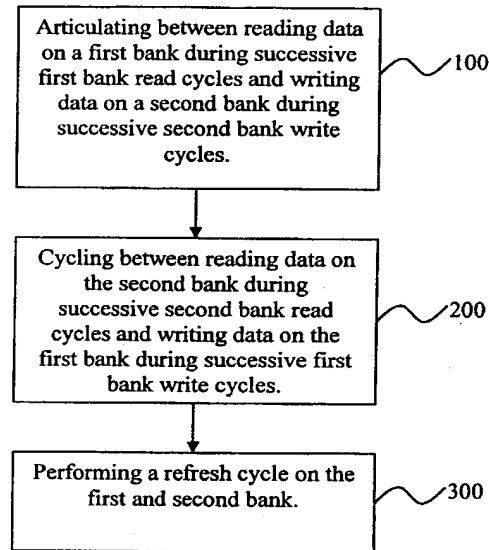
DETAILED TIMING WITH INTERNAL RAM OPERATION
QDR 4-Burst 2-Bank Timing Diagram: Read-Write



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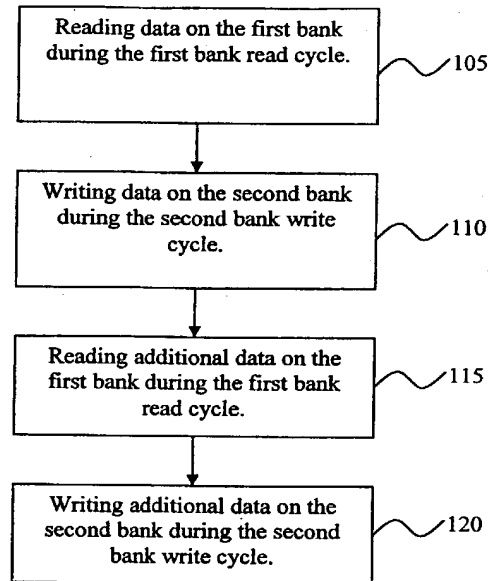
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Figure 6



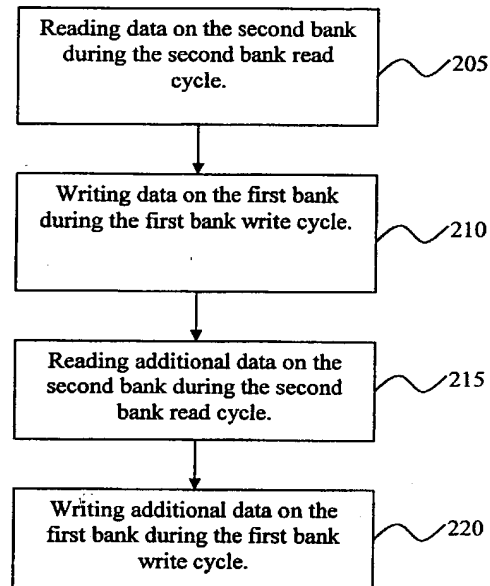
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Figure 7



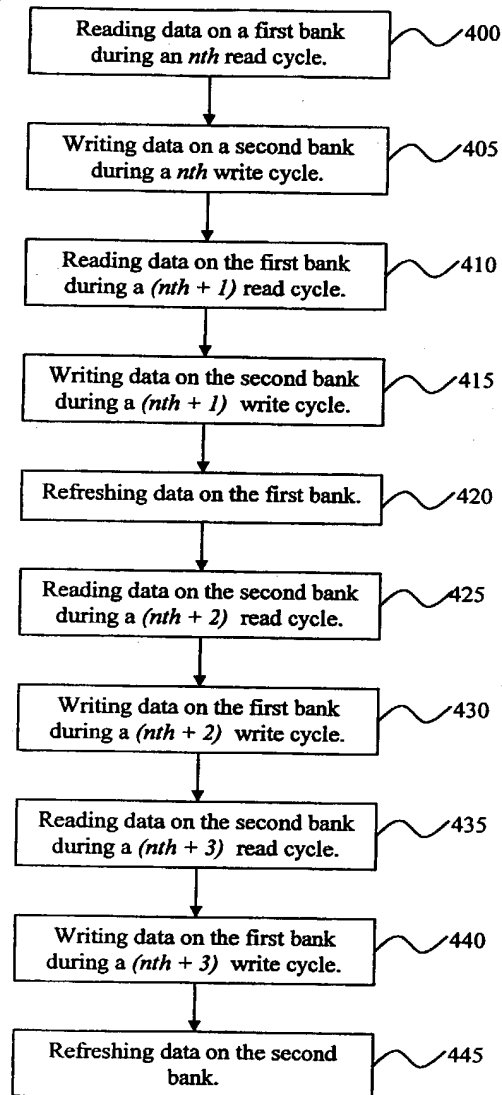
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Figure 8



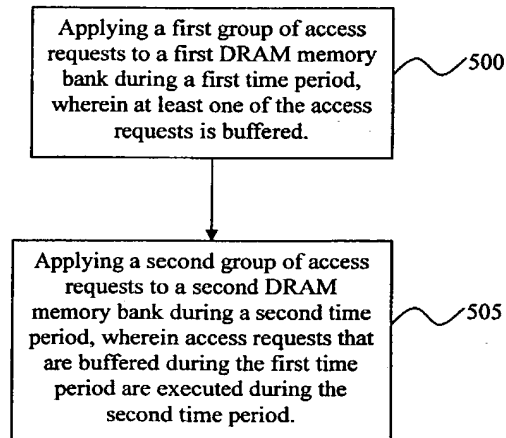
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Figure 9



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Figure 10



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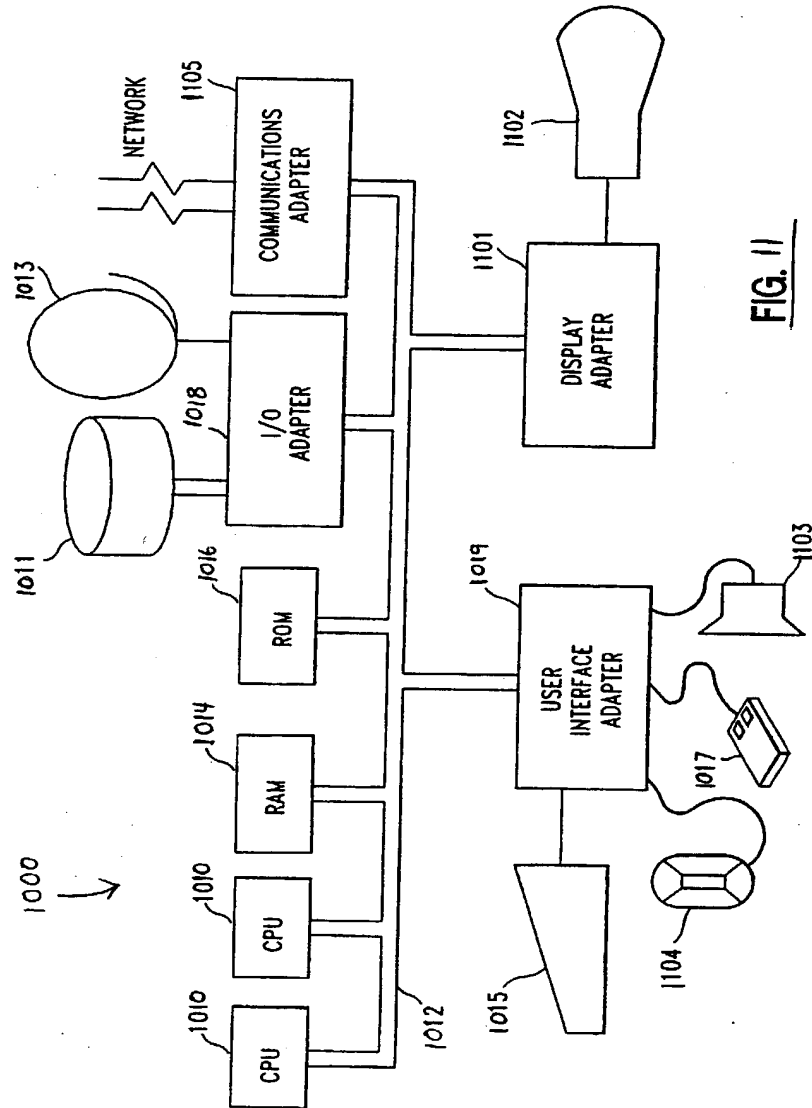


FIG. 11